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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/646,029	08/22/2003	Ming-shuan Yeh	JLINP166	9261

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EXAMINER

HAROON, ADEEL

ART UNIT PAPER NUMBER

2685

DATE MAILED: 12/19/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/646,029	YEH, MING-SHUAN	
	Examiner	Art Unit	
	Adeel Haroon	2685	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-15 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. ____. |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

2. Claims 1-5, 11, and 12 rejected under 35 U.S.C. 102(e) as being anticipated by Satoh (U.S. 6,895,228).

With respect to claim 1, Satoh discloses a switchable high frequency bandpass filter in figure 4. Satoh discloses an input node, element number 1, and an output node,

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element number 2 (Column 3, lines 58-61). Satoh also discloses a switchable LC resonator coupled between the input node and output node for providing a plurality of switchable filter transfer functions for a plurality of high frequency signals, GSM and DCS bands, having different frequencies transmitted from the input node to the output node (Column 3, lines 58-65). Satoh further discloses a switch signal input interface circuit coupled to the switchable LC resonator for controlling the switchable LC resonator to provide the plurality of high frequency signals having different frequencies with a suitable one of the plurality of switchable filter transfer functions (Column 3, line 66 – Column 4, line 4).

With respect to claim 2, Satoh discloses the switchable LC resonator comprising an inductive unit, element numbers 26, 27, 29, and 21, coupled between the input node and ground in figure 4. Satoh also discloses a first capacitive unit, element numbers 19 and 20, coupled between the input node and ground such that the inductive unit and the first capacitive unit construct a first-state parallel LC resonant circuit (Column 5, lines 7-12). Satoh further discloses a second capacitive unit, element numbers 22 and 28, between the input node and ground such that the inductive unit, the first capacitive unit, and the second capacitive unit construct a second-state parallel LC resonant circuit (Column 5, lines 22-28).

With respect to claim 3, Satoh discloses the switch signal is a DC voltage signal having a predetermined lower and higher voltage levels where the second capacitive unit is enabled and disabled according to the DC voltage signal resulting in that the switchable LC resonator switches configuration between the first state parallel LC

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resonant circuit and the second sate parallel LC resonant circuit (Column 4, lines 45-54).

With respect to claim 4, Satoh discloses a blocking circuit, element numbers 11 and 15, having a first terminal coupled to the output node and second terminal used as a common high frequency output terminal which is used to have sharp attenuation at harmonics such as DC signals (Column 4, lines 55-61).

With respect to claim 5, Satoh further discloses a capacitive element, element number 11, in the DC blocking circuit having a terminal used as the first terminal and another terminal used as the second terminal (Column 4, lines 55-61).

With respect to claim 11, Satoh further discloses that the inductive unit comprises of a first inductor, element number 29, having a terminal coupled to a DC voltage source, ground since it is zero DC volts, and another terminal coupled to both of the input and output node in figure 4. Satoh also discloses a first capacitor, element number 21, coupled to the DC voltage source, the ground right below it since ground is zero DC volts (Column 5, lines 6-12).

With respect to claim 12, Satoh further discloses the first capacitive unit comprising a second capacitor, element number 19, having a terminal coupled to both the input and output nodes and another terminal coupled to ground in figure 4.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 6-10 are rejected under 35 U.S.C. 103(a) as being unpatentable over Satoh in view of Damgaard et al. (U.S. 6,208,875).

With respect to claims 6-8, the switchable high frequency bandpass filter of Satoh is described above in the discussion of claims 1 and 2. Satoh further discloses that the switchable high frequency bandpass filter is used in a dual band, GSM and DCS, transmitter having frequency generated signals at GSM and DCS bands entering at element number 102 in figure 11 (Column 1, lines 17-31). Satoh also discloses controlling the switch signal according to the type of signal being inputted at the filter (Column 4, lines 45-51). Satoh does not expressly disclose a high frequency signal generator. However, Damgaard et al. disclose a dual band, GSM and DCS, transmitter thus making it analogous art with Satoh since it is in the same field of endeavor. Damgaard et al. teach high frequency signal generator, element number 15, that has a first and second frequency generating circuit for generating signals at two different frequencies, for generating the plurality of high frequency signals entering a filter/buffer

element, 23 (Column 3, line 61 – Column 4, line 3). Therefore, it would be obvious to one of ordinary skill in the art at the time of the applicant's invention to apply the high frequency signal generator of Damgaard et al. in the switchable high frequency bandpass filter of Satoh in order to control the frequencies of the signals generated.

With respect to claim 9, Both Satoh and Damgaard et al. teach using DCS and GSM bands where GSM band is substantially twice as high as the DCS band (Satoh Column 1, lines 17-25 and Damgaard et al. Column 3, lines 39-45).

With respect to claim 10, Damgaard et al. further discloses that the first and second frequency generation circuits are formed by a voltage-controlled oscillator, element number 57 and 59 (Column 4, lines 44-60).

5. Claims 13-15 are rejected under 35 U.S.C. 103(a) as being unpatentable over Satoh in view of Watanabe et al. (U.S. 6,937,845).

With respect to claims 6-8, the switchable high frequency bandpass filter of Satoh is described above in the discussion of claims 1 and 2. Satoh also discloses that second capacitive unit comprising a third capacitor, element number 22/17, having a terminal coupled to both the input and output nodes (column 5, lines 22-24). Satoh also discloses a diode, element number 17, having a P electrode for receiving the switch signal from element number 18 and an N electrode, where the P electrode is coupled to another terminal of the third capacitor and the switch signal input interface (Column 5, lines 29-39). Satoh does not expressly disclose a fourth capacitor and resistor.

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However, Watanabe et al. disclose a high frequency module working in dual bands, GSM and DCS, thus making it analogous art with Satoh since it is in the same field of endeavor. Watanabe et al. teaches using a switching diode, element number D2, to change the transmission frequency where a fourth capacitor, element number C23, and resistor, element number R, are each coupled in series between the N electrode of the diode and ground (Column 4, lines 36-41). Therefore, it would be obvious to one of ordinary skill in the art at the time of the applicant's invention to apply the added load of a capacitor and resistor technique of Watanabe et al. in the switchable high frequency bandpass filter of Satoh in order to have a load in between the diode and ground to ensure better impedance.

With respect to claim 14, Satoh discloses the switch signal is a DC voltage signal having a predetermined lower and higher voltage levels where the diode is enabled and disabled according to the DC voltage signal (Column 4, lines 45-54).

With respect to claim 15, Satoh further discloses a signal input interface comprising a second capacitor, element number 19, having a terminal coupled to the P electrode of the diode and another terminal for receiving the switch signal and a fifth capacitor, element number 20, having a terminal coupled to the another terminal, for receiving the switch signal and is coupled to ground in figure 4.

Conclusion

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Lange (U.S. 6,064,866) discloses a switchable bandpass filter

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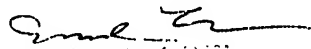
for a multiband tuner. Kupfer (U.S. 4,736,457) discloses a bandpass filter working over two frequency bands.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Adeel Haroon whose telephone number is (571) 272-7405. The examiner can normally be reached on Monday thru Friday, 8:30 a.m. - 5:00 p.m..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward Urban can be reached on (571) 272-7899. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

AH
11/29/05


ADEEL HAROON
Examiner
Art Unit 2685
November 29, 2005